

Notice of Allowability

Application No.

10/665,175

Examiner

My-Trang N. Ton

Applicant(s)

JOO, JIN-TAE

Art Unit

2816

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Tel. Int. 09/15/04.
2. ☒ The allowed claim(s) is/are 1-45.
3. ☐ The drawings filed on _____ are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☒ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☒ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date 09/16/04.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date 09/16/04.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

EXAMINER'S AMENDMENT

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it **MUST** be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Att. Mills on September 15, 2004.

The application has been amended as follows:

In the drawings

The following changes to the drawings have been approved by the examiner and agreed upon by applicant: Figs. 1A, 1B, 1C, 2A and 2B have been labeled "Prior Art". In order to avoid abandonment of the application, applicant must make these above agreed upon drawing changes.

In the claims

In claim 4, line 1, replace "further" with – wherein the clock circuit --.

In claim 5, line 1, replace "further" with – wherein the clock circuit --.

In claim 6, line 1, replace "further" with – wherein the clock circuit --.

In claim 7, line 1, replace "further" with – wherein the reset circuit --.

In claim 11, line 1, replace "further" with – wherein the clock circuit --.

In claim 12, line 1, replace "further" with – wherein the clock circuit --.

In claim 19, line 1, replace "further" with – wherein generating the internal clock signal --.

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In claim 20, line 1, replace "further" with – wherein generating the internal clock signal --.

In claim 21, line 1, replace "further" with – wherein generating the internal clock signal --.

In claim 22, line 1, replace "further" with – wherein generating the internal reset signal --.

In claim 26, line 1, replace "further" with – wherein generating the internal clock signal --.

In claim 27, line 1, replace "further" with – wherein generating the internal clock signal --.

In claim 30, line 1, replace "further" with – wherein masking the internal clock signal --.

In claim 34, line 1, replace "further" with – wherein the clock circuit --.

In claim 35, line 1, replace "further" with – wherein the clock circuit --.

In claim 36, line 1, replace "further" with – wherein the clock circuit --.

In claim 37, line 1, replace "further" with – wherein the reset circuit --.

In claim 41, line 1, replace "further" with – wherein the clock circuit --.

In claim 42, line 1, replace "further" with – wherein the clock circuit --.

Reasons for allowance

The following is an examiner's statement of reasons for allowance:

The present invention is drawn to an apparatus for regenerating reset and clock signals utilizing a clock circuit, an external clock signal, a plurality of clocked circuits, a

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reset circuit, an external reset signal, a clock masking circuit, a counter circuit, first – third comparator circuits, and a mask delay circuit recited in claims 1-45. None of the prior art disclosed or suggested to show the particular structure and/or the particular operation recited in these claims namely: the combination of “the clock circuit ... being forward to a plurality of clocked circuits to clock the clocked circuits”, “the reset circuit ... being forwarded to the plurality of clocked circuits to reset the clocked circuits” and “the clock masking circuit for masking the internal clock signal for a masking period such that the clocked circuits are not clocked during the masking period” as recited in claims 1 and 31; the combination of all the method steps as recited in claim 16.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled “Comments on Statement of Reasons for Allowance.”

Any inquiry concerning this communication or earlier communications from the examiner should be directed to My-Trang N. Ton whose telephone number is 571-272-1754. The examiner can normally be reached on 7:00 a.m - 5:30 p.m.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



**MY-TRANG NUTON
PRIMARY EXAMINER**

September 16, 2004